



**BEST AVAILABLE COPY**

PATENT NUMBER

## U.S. UTILITY Patent Application

JCS88

9

**O.I.P.E.**

**PATENT DATE**

**SCANNED**

**Q.A.**

Q.A. Am

APPLICATION NO. 09/683872	CONT/PRIOR	CLASS 257	SUBCLASS 459	ART UNIT 2811 2815	EXAMINER <i>Goldie Lee</i>
------------------------------	------------	--------------	-----------------	--------------------------	-------------------------------

Joseph Iadanza

Orth

# TITLE

### Method of connecting core I/O pins to backside chip I/O pads

PTO-2040  
12/89

## ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b>	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	

**WARNING:**

The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

Form PTO-436A  
(Rev. 6/89)

FILED WITH: ☐ DISK (CRF) ☐ FICHE ☐ CD-ROM  
(Attached in pocket on right inside flap)

**(FACE)**